Ref: KSSEM/ECE/ CIR-GEN/045/2021-22   Date: 06/5/2022

CIRCULAR

Department of Electronics and Communication Engineering is organizing Technical Talk on **"VLSI –Industry Perspective"** through Online Mode on 10/5/2022 at 10.45 AM.

Venue: Aryabhatta Seminar Hall

Audience: 8th Semester Students

Faculty Coordinator   Head of the Department

Mrs. Shilpa A P, Asst. Professor   Professor & Head
                                                        Dept. of Electronics & Communication Engineering
Mr. Puneeth S, Asst. Professor   K. S. School of Engineering & Management
                                                        Bangalore-560 109

Note: On 10th from 10.45 to 12.30pm classes will be conducted on the same day afternoon. Attend the classes without fail

6/5/22

Professor & Head
Dept. of Electronics & Communication Engineering
K. S. School of Engineering & Management
Bangalore-560 109

TAKSHILA VLSI
Training for a Smarter Career

Job Oriented Training

ASIC DESIGN & VERIFICATION

Duration: 5 months

100% Placement
Guarantee for 2019/20/21/22 Pass-out Students

📞 ⓢ 97429 72744

Next batch starting from
9th May 2022

- Eligibility: B.E/B.Tech in ECE/EEE/EIE, M.E/M.Tech/M.S Studying or Completed
- Affordable Fee with Industry standard EDA Tools
- Mode of Study: Full Time Online and Weekend Online
- Complete suite of job oriented ASIC Verification course and get instant job in VLSI domain with minimum 4-6L salary.

TAKSHILA VLSI
Training for a Smarter Career

PHYSICAL DESIGN COURSE

Placements ongoing with multiple VLSI Companies.

with 100% Placement Guarantee for 2019/20/21/22 Pass-out Students

CALL NOW
97429 72744

Next batch starting from
29th April 2022

Available:
Online / Full Time / Part Time (Weekend – Online)

Duration:
5 months

Eligibility:
B.E/B.Tech in E.C.E/E.E.E/E.I.E, M.E/M.Tech/M.S Studying or Completed

Affordable Fee with Industry standard EDA Tools

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Online Technical Talk on "VLSI INDUSTRY PERSPECTIVE"

Department of ECE organized online technical talk on VLSI Industry and Job Opportunities in Semiconductor Industry on **10/5/2022** for Eighth Semester ECE Students from **"Takshila VLSI"** Training for a Smarter Career.

Zoom Meeting Link:
https://us06web.zoom.us/j/87931297215?pwd=eTNndTU3Q2pBS3RHeno0dlA4bmptQT09

Zoom Meeting Id:
Meeting ID: 879 3129 7215
Passcode: 752554

This Technical Talk given insights of verilog coding, EDA tools and various domain jobs in VLSI field such as Analog Design, Physical Design Job, Design Verification Job, and Design for Test Jobs.
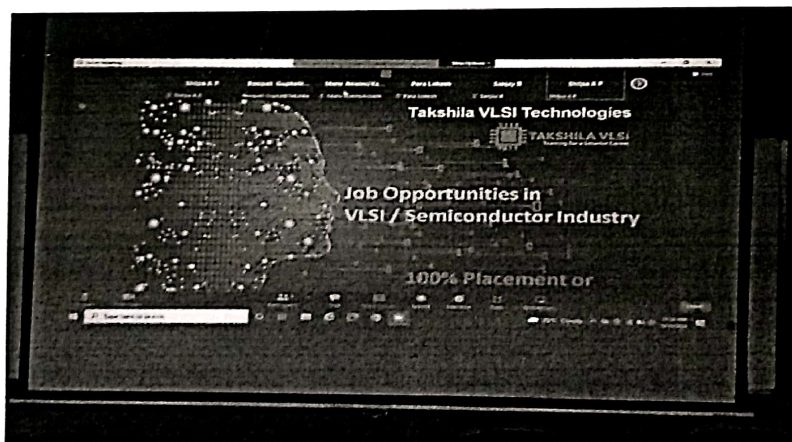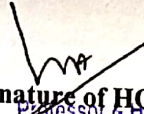

Fig: Inauguration

Fig: Interactive Session

Signature of HOD
Professor & Head
Dept. of Electronics & Communication Engineering
K. S. School of Engineering & Management
Bangalore-560 109